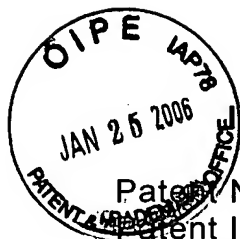


Cafc



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Patent No. 6,849,493 B2
Patent Issue Date February 1, 2005
Application Serial No. 10/671,228
Filing Date September 24, 2003
Assignee Micron Technology, Inc.
Inventorship Shubneesh Batra, et al.
Attorney's Docket No. MI22-2397
Title: Methods of Forming Polished Material and Methods of Forming Isolation Regions

REQUEST FOR CERTIFICATE OF CORRECTION OF PATENT
FOR PTO MISTAKE (37 C.F.R. 1.322(a))

Certificate

To: Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
ATTN: Decision and Certificate of Correction
Branch of the Patent Issue Division

JAN 27 2006

of Correction

From: D. Brent Kenady (Tel. 509-624-4276; Fax 509-838-3424)
Wells St. John P.S.
601 W. First Avenue, Suite 1300
Spokane, WA 99201-3828

Sir:

It is hereby requested that a Certificate of Correction be issued with respect to Patent No. 6,849,493 B2, granted February 1, 2005, in accordance with the Certificate of Correction form attached hereto in duplicate.

The errors listed on the Certificate of Correction form were apparently incurred through the fault of the PTO as will be disclosed by the records of files in the Office.

Since this Certificate of Correction is being requested due to PTO errors, it is believed that no fee is due. However, in the event that a fee is required for issuance of this Certificate of Correction, please charge the fee specified under

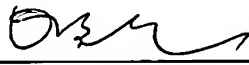
JAN 30 2006

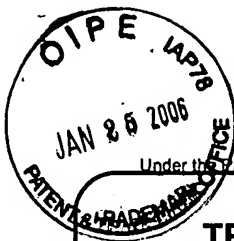
37 C.F.R. § 1.20(a) to Deposit Account No. 23-0925. Please credit Deposit Account No. 23-0925 with any overpayment of the above fee.

Attached hereto, in duplicate is Form PTO-1050, with at least one copy being suitable for printing.

Respectfully submitted,

Dated: 1-20-06

By: 
D. Brent Kenady
Reg. No. 40,045



PTO/SB/21 (09-04)

Approved for use through 07/31/2006. OMB 0651-0031

U.S. Patent and Trademark Office; U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

**TRANSMITTAL
FORM**

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number	10/671,228
Filing Date	September 24, 2003
First Named Inventor	Shubneesh Batra, et al.
Art Unit	2823
Examiner Name	Hsien Ming Lee
Attorney Docket Number	MI22-2397

ENCLOSURES (Check all that apply)

<input type="checkbox"/> Fee Transmittal Form	<input type="checkbox"/> Drawing(s)	<input type="checkbox"/> After Allowance Communication to TC
<input type="checkbox"/> Fee Attached	<input type="checkbox"/> Licensing-related Papers	<input type="checkbox"/> Appeal Communication to Board of Appeals and Interferences
<input type="checkbox"/> Amendment/Reply	<input type="checkbox"/> Petition	<input type="checkbox"/> Appeal Communication to TC (Appeal Notice, Brief, Reply Brief)
<input type="checkbox"/> After Final	<input type="checkbox"/> Petition to Convert to a Provisional Application	<input type="checkbox"/> Proprietary Information
<input type="checkbox"/> Affidavits/declaration(s)	<input type="checkbox"/> Power of Attorney, Revocation	<input type="checkbox"/> Status Letter
<input type="checkbox"/> Extension of Time Request	<input type="checkbox"/> Change of Correspondence Address	<input checked="" type="checkbox"/> Other Enclosure(s) (please identify below):
<input type="checkbox"/> Express Abandonment Request	<input type="checkbox"/> Terminal Disclaimer	Return Receipt Postcard; Request for Certificate of Correction; Certificate of Correction (2)
<input type="checkbox"/> Information Disclosure Statement	<input type="checkbox"/> Request for Refund	
<input type="checkbox"/> Certified Copy of Priority Document(s)	<input type="checkbox"/> CD, Number of CD(s) _____	
<input type="checkbox"/> Reply to Missing Parts/Incomplete Application	<input type="checkbox"/> Landscape Table on CD	
<input type="checkbox"/> Reply to Missing Parts under 37 CFR 1.52 or 1.53	Remarks	
	Customer No. 021567 Patent No. 6,849,493 B2 Issued: February 1, 2005	
	No Fee Required. However, should a fee be determined as owing, charge such fee to Deposit Account No. 23-0925	

SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm Name	Wells St. John P.S.		
Signature			
Printed name	D. Brent Kenady		
Date	1-20-06	Reg. No.	40,045

CERTIFICATE OF TRANSMISSION/MAILING

I hereby certify that this correspondence is being facsimile transmitted to the USPTO or deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below:

Signature			
Typed or printed name	Terri Lynn Boyd-Davis	Date	1-23-06

This collection of information is required by 37 CFR 1.5. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.11 and 1.14. This collection is estimated to 2 hours to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.

JAN 30 2006

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,849,493 B2
DATED : February 1, 2005
INVENTOR(S) : Shubneesh Batra, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification

Col. 1, line 37 -

Replace "a is semiconductive wafer fragment **10** is shown at a"
With "a semiconductive wafer fragment **10** is shown at a"

Col. 2, line 30 -

Replace "**52, 54, 56** and **58** within openings **22, 24, 26, 29** and **30**,"
With "**52, 54, 56** and **58** within openings **22, 24, 26, 28** and **30**,"

Col. 7, line 65 -

Replace "layer joining the upper layer at an interlace, the"
With "layer joining the upper layer at an interface, the"

Col. 8, line 61 -

Replace "interlace is elevationally proximate the uppermost surface of"
With "interface is elevationally proximate the uppermost surface of"

Mailing Address of Sender:

D. Brent Kenady
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Patent No. 6,849,493 B2

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

Page 1 of 1

PATENT NO. : 6,849,493 B2
DATED : February 1, 2005
INVENTOR(S) : Shubneesh Batra, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In the Specification

Col. 1, line 37 -

Replace "a is semiconductive wafer fragment 10 is shown at a"
With "a semiconductive wafer fragment 10 is shown at a"

Col. 2, line 30 -

Replace "52, 54, 56 and 58 within openings 22, 24, 26, 29 and 30,"
With "52, 54, 56 and 58 within openings 22, 24, 26, 28 and 30,"

Col. 7, line 65 -

Replace "layer joining the upper layer at an interlace, the"
With "layer joining the upper layer at an interface, the"

Col. 8, line 61 -

Replace "interlace is elevationally proximate the uppermost surface of"
With "interface is elevationally proximate the uppermost surface of"

Mailing Address of Sender:

D. Brent Kenady
Wells St. John P.S.
601 West First Avenue, Suite 1300
Spokane, WA 99201-3828

Patent No. 6,849,493 B2